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Patent
Attorney's Docket No. 009683-488

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	MAIL STOP AMENDMENT
Tatsuo Sengoku et al.)	Group Art Unit: 2859
Application No.: 10/730,123)	Examiner: Stanley J. Pruchnic
Filed: December 9, 2003)	Confirmation No.: 4345
For: SEMICONDUCTOR INTEGRATED)	
CIRCUIT WITH SIMPLY)	
CONFIGURED TEMPERATURE)	
DETECTION CIRCUIT)	

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: December 13, 2004

Sir:

This election is filed in response to the Office Action dated November 16, 2004 (Paper No. 20041110). In the Office Action, the Examiner identified two distinct inventions. The Examiner required the Applicants, under 35 U.S.C. §121, to elect a single invention for prosecution. The two inventions identified by the Examiner are:

- I) Claims 1-3, drawn to a semiconductor integrated circuit comprising a temperature detection circuit,
- II) Claims 4-15, drawn to a semiconductor integrated circuit comprising a temperature detection circuit.

Pursuant to the Examiner's requirement, the Applicants elect Group I, claims 1-3. It is understood that claims 4-15 will be withdrawn from prosecution. Applicants reserve the right to file divisional applications.

An earlier examination and favorable action on claims 1-3 are respectfully requested.

In the event this paper is not considered to be timely filed, Applicants hereby petition for an appropriate extension of time. The Commissioner is authorized to charge payment for any additional fees which may be required with respect to this paper to our Deposit Account No. 02-4800.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: December 13, 2004

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